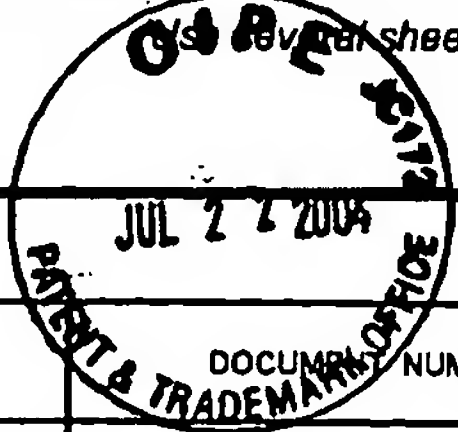


INFORMATION DISCLOSURE CITATION



ATTY DOCKET NO. BUR9200300176US1		SERIAL NO. 10/709327
Hathaway et al.		
FILING 4-28-04	GROUP Unknown	2825

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
BB	JP8314991A	06/09/1998	Japan			✓	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/BB/		W.E. Donath, E.M. Sharp, M.L. White, "Performance Evaluation For Automated Layout Procedures", IBM Technical Disclosure Bulletin, Vol. 25, No. 10, March 1983, Pages 5049-5051
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EXAMINER /Brandon Bowers/	DATE CONSIDERED 10/15/2007
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

METHOD OF IDENTIFYING PATHS WITH DELAYS
DOMINATED BY A PARTICULAR FACTOR

Application Number :

Confirmation Number:

First Named Applicant: David Hathaway

Attorney Docket Number: BUR920030176US1

Art Unit:

Examiner:

Search string: (5751593 or 5838581 or 5845233 or 5896301 or 5903472 or 6014510 or 6209122
or 5359535 or 6412101 or 6449754 or 6553550).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
/BB/	1	5751593	1998-05-12	Pullela et al.			
↓	2	5838581	1998-11-17	Kuroda			
↓	3	5845233	1998-12-01	Fishburn			
↓	4	5896301	1999-04-20	Barreintos			
↓	5	5903472	1999-05-11	Barreintos			
↓	6	6014510	2000-01-11	Burks et al.			
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↓	9	6412101	2002-06-25	Chang et al.			
↓	10	6449754	2002-09-10	You et al.			
↓	11	6553550	2003-04-22	Menegay et al.			

Signature

Examiner Name	Date
/Brandon Bowers/	10/15/2007